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NEW APPLICATION TRANSMITTAL

Transmitted herewith for filing is the patent application of:

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For (title): BLOCK-ITERATIVE EQUALIZERS FOR DIGITAL COMMUNICATION SYSTEM

1. Type of Application

Utility
Design

2. Benefit of Prior U.S. Application(s) Under 35 U.S.C. §120

2.	Benefit of Prior U.S. Application	(s) Under 35 U.S.C. §120		
	This application is a:			
	Divisional			
	Continuation			
	Continuing Patent A	oplication (CPA)		
	Continuation-in-part	(CIP),		
	and hereby claims benefit under 35 U.S.C. §120 to the following applications			
	SERIAL NUMBER	FILING DATE		
	None			

3. Benefit of Non-U.S. Application Under 35 U.S.C. §119(a)-(d)
This application claims priority under 35 U.S.C. §119(a)-(d) to the following foreign application(s) and/or inventor certificate(s):

None FILING DATE

Certified copy(ies) of the application(s) and/or inventor certificate's from which priority is claimed:

is(are) attached; will follow.

4. Benefit of Provisional Application Under 35 U.S.C. §119(e)

This application claims priority to the following provisional application(s):

SERIAL NUMBER	FILING DATE	
None		

CERTIFICATE OF EXPRESS MAIL UNDER 37 C.F.R. §1.10

I hereby certify that this New Application Transmittal and the documents referred to as enclosed therein are being deposited with the United States Postal Service on March 20, 2000 in an envelope as "Express Mail Post Office to Addressee" Mailing Label Number

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1 of 3

5. 43 4	Papers Enclosed Which Are Required For Filing Date Under 37 C.F.R. §1.53 Pages of Specification, including claims and abstract Sheets of Drawings Additional Papers Enclosed Declaration and Power of Attorney (unsigned) Preliminary Amendment Information Disclosure Statement (37 CFR 1.98), Form PTO-1449 and a copy of each cited reference Assignment and Form PTO-1595 Small Entity Declaration This application is/will be assigned to insert name of assignee if claiming sm. ent Declaration of Biological Deposit Submission of "Sequence Listing" computer readable copy and/or amendment pertaining thereto for biotechnology invention containing nucleotide and/or amino acid sequences. Other		
6.			
7.	Application Filing Fee Calculation A. Utility Application FEE CALCULATION: Total Claims: 32 - 20 = 12×\$18 = \$216.00 Independent Claims: 8 - 3 = 5 ×\$78 = \$390.00 Basic Fee: \$760.00 Multiple-Dependent-Claim Fee: \$ Total of the Above Calculations: \$1,360 Amendment canceling extra claims enclosed. Amendment deleting multiple dependencies enclosed. Fee for extra claims is not being paid at this time. B. Design application - \$310 Application Filing Fee Sub-Total C. Less 50% reduction for small entity D. Non-English Specification - \$130	\$ \$ \$ \$ \$- \$	
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The Commissioner is hereby authorized to charge any fees under 37 C.F.R. §§1.16 and 1.17 that may be required by this paper or any paper filed in connection with this Patent Application, or refund any overpayment to our Deposit Order Account No. 19-0079.

Respectfully submitted,

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BLOCK-ITERATIVE EQUALIZERS FOR DIGITAL COMMUNICATION SYSTEMS

GOVERNMENT SUPPORT

The Government has rights in this invention pursuant to Contract Number N00014-96-1-0930, awarded by the Office of Naval Research.

FIELD OF THE INVENTION

The present invention relates to a novel class of equalizers for use in the receivers of digital communication systems.

BACKGROUND OF THE INVENTION

In the receiver of a contemporary digital communication system, an equalizer operates to compensate for various forms of distortion introduced by the propagation medium. In wireless communication systems for example, an important source of such distortion is time-varying multipath propagation, whereby the transmitted signal travels through multiple paths en route to the receiver due to reflections off objects in the propagation environment. In wireline communication, such as twisted copper wire pair or co-axial cable, such distortion arises due to the frequency response characteristics of the physical medium, referred to herein as the "channel", or of other system hardware in the network.

Absent some form of correction or compensation, such distortion can cause streams of transmitted bits or symbols to interfere with one another, a phenomenon generally referred to as intersymbol interference. In certain code-division multiple-access (CDMA) communication systems, distortion may further give rise to a phenomenon referred to as inter-chip interference. Intersymbol interference (ISI) is used herein to refer to both forms of interference. Precursor ISI is caused by the strictly anticausal portion of the equivalent discrete-time channel impulse response, whereby a current symbol affects symbols in the past. Postcursor ISI is caused by the strictly causal portion of the equivalent discrete-time channel impulse response, whereby a current symbol

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affects symbols in the future. ISI can lead to severe degradation in system performance, especially in wireless settings. Equalizers, designed to compensate for, and to mitigate the effects of, such interference, have been under development for more than three decades and have evolved to become essential components in virtually all modern communication systems.

It is well known in theory that receivers that realize maximum-likelihood sequence detection (MLSD) are asymptotically optimal in bit-error rate at high signal-to-noise ratios (SNR), as observed in G. D. Forney, Jr., "Maximum-likelihood sequence estimation of digital sequences in the presence of intersymbol interference," *IEEE Trans. Inform. Theory*, vol. IT-18, pp. 363-378, May 1972. However, even when implemented efficiently using the Viterbi algorithm, the computational requirements of such receivers are prohibitive for even small signal constellations and channels whose ISI spans only a modest number of symbols. Indeed, although the complexity of MLSD is independent of block-length, it does grow as M^{L-1} where M is the size of the symbol alphabet and L is the length of the impulse response of the equivalent discrete-time channel. As a result, practical equalizers rarely employ MLSD.

Two approaches have become popular in practice: linear equalizers (LEs) and decision-feedback equalizers (DFEs). LEs were developed in the 1960's, and employed linear filters to compensate for distortion. Later, in the late 1960's and early 1970's, the inherently non-linear DFEs were introduced. In both cases, the computational complexity is dramatically lower than the theoretically superior MLSD approach - it is essentially independent of the size of the symbol alphabet M and proportional to the length of the channel impulse response L.

Like the LE, the DFE processes the received signal using a linear filter and makes symbol decisions using a slicer, as disclosed in C. A. Belfiore and J. H. Park, Jr., "Decision-feedback equalization," *Proc. IEEE*, vol. 67, pp. 1143-1156, Aug. 1979. However, each time a decision is to be made, a linear, strictly causal filter forms a weighted linear combination of previous symbol decisions, assumed to be correct, to cancel the postcursor ISI at the slicer input. The slicer then generates a decision for the

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current symbol. Note that once a symbol decision is made, it does not change and is used to cancel postcursor ISI when making decisions for future symbols.

Another equalizer approach, referred to as the "ISI canceler", processes the received signal using a linear filter and makes symbol decisions using a slicer, as disclosed in Gersho and T. L. Lim, "Adaptive cancellation of intersymbol interference for data transmission," Bell Syst. Tech. J., vol. 60, pp. 1997-2021, Nov. 1981. However, each time a decision is to be made, a linear noncausal filter forms a weighted linear combination of both previous and future tentative symbol decisions made by some other equalizer, typically a linear equalizer, to cancel ISI at the slicer input. The slicer then generates a final decision for the current symbol.

LEs are widely used, but often suffer from excessive noise enhancement, even when minimum mean-square error (MMSE) design criteria are used. DFEs are capable of better performance, particularly at higher SNR, because postcursor ISI is suppressed nonlinearly. However, noise enhancement is still an issue in DFEs because precursor ISI is only suppressed linearly, and the sequential structure of DFE algorithms make them generally incompatible with error-control channel coding due to decoding delay issues. despite numerous attempts to merge the two; see, for example, (a) J. G. Proakis, Digital Communications. New York, NY: McGraw-Hill, 2nd ed., 1989. (b) V. M. Eyuboglu, "Detection of coded modulation signals on linear, severely distorted channels using decision-feedback noise prediction with interleaving," IEEE Trans. Commun., vol. COM-36, Apr. 1988. (c) K. Zhou and J. G. Proakis, "Coded reduced-bandwidth QAM with decision-feedback equalization," in *Proc. Int. Conf. Commun.*, pp. 12.6.1-12.6.5, June 1988. (d) K. Zhou, J. G. Proakis, and F. Ling, "Decision-feedback equalization of fading dispersive channels with trellis-coded modulation," in Proc. Int. Conf. Commun. Techn., Nov. 1987. Since error-control coding is required to approach channel capacity, an equalizer compatible with this feature is highly desirable.

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SUMMARY OF THE INVENTION

The present invention represents a major improvement over existing equalizers, and its advantages are the result of a fundamentally new equalizer architecture.

The novel class of equalizers presented herein are configured in a block-iterative structure, i.e., they are implemented by a multiple-pass algorithm. An illustrative example of the operation of the new class of equalizers is as follows. Let x[n] denote an uncoded transmitted symbol stream and r[n] denote a received data stream corresponding to the equivalent discrete-time baseband model. On the first pass, this received data is processed by a linear filter, and the resulting output is passed on to a slicer, which makes a first set of tentative symbol decisions $\hat{x}^1[n]$. This initial pass is then followed by a second pass in which the same received data is processed by a linear filter, and the tentative decisions made in the previous pass are used to construct and subtract out an estimate of the ISI. The ISI-reduced data is then passed on to the slicer, which makes a new second set of tentative decisions $\hat{x}^2[n]$. The process repeats in this manner, until adequately reliable decisions are obtained.

Unlike the conventional DFEs, the novel equalizers of the present invention suppress virtually all of the ISI, including precursor and postcursor ISI, in a nonlinear manner, significantly enhancing system performance. In fact, theory and simulations have demonstrated that the new class of equalizers can achieve a given level of reliability (as measured by bit-error rate) using significantly less received signal power than conventional equalizers.

Additionally, unlike the DFE, the equalizers of the present invention can be deployed in conjunction with systems employing error-control coding, making them attractive for channels operating near capacity.

A further advantageous feature of the equalizers of the present invention is extremely low computational complexity. The order of complexity is comparable to that of the DFE, and the adaptive implementation is much less computationally complex.

In a first embodiment, the present invention is directed to an apparatus and method for iteratively equalizing received data transmitted over a data channel in a data

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communication system. At each iteration, a first filter, for example a "feed-forward" filter, filters received data according to first filter parameters to generate first-filtered data. A combiner modifies the first-filtered data with second-filtered data to generate modified data. A decision device, for example a digital slicer generates modified tentative decisions based on the modified data, the modified tentative decisions being modified with respect to tentative decisions of a previous iteration. A second filter, for example a "feed-back" filter, filters tentative decisions from a previous iteration according to second filter parameters to generate said second-filtered data. The first and second filter parameters are based on the received data, and are preferably modified at each iteration.

In a preferred embodiment, the received data is sampled at a rate higher than a symbol rate associated with the received data. The received data may comprise symbol data.

The first and second parameters may be modified at each iteration according to channel parameters that are re-estimated at each iteration based on the received data.

The received data may optionally be encoded for error correction coding, in which case the decision device comprises an error-correction decoder, and wherein the equalizer further comprises an encoder for encoding the tentative decisions from a previous iteration.

The feed-forward and feed-back filters may comprise linear, non-linear, time-variant, time-invariant, infinite-impulse-response (IIR) and finite-impulse-response (FIR) types.

In alternative embodiments, the received data may comprise a plurality of received signals received over a plurality of data channels, in which case the equalizer further comprises a like plurality of first filters corresponding to the plurality of channels. The received data may further comprise combined data for a plurality of users, in which case the equalizer further comprises a like plurality of second filters corresponding to the plurality of users.

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BRIEF DESCRIPTION OF THE DRAWINGS

The foregoing and other objects, features and advantages of the invention will be apparent from the more particular description of preferred embodiments of the invention, as illustrated in the accompanying drawings in which like reference characters refer to the same parts throughout the different views. The drawings are not necessarily to scale, emphasis instead being placed upon illustrating the principles of the invention.

- FIG. 1 is a graph comparing uncoded quadrature phase-shift keying (QPSK) performance predictions for the iterative equalizer of the present invention and various classical equalizers, based on asymptotic (long ISI) analysis.
- FIG. 2 is a block diagram of a data signal receiver configuration in accordance with the present invention.
- FIG. 3 is a block diagram of an equalizer system in accordance with the present invention.
- FIG. 4 is a block diagram of a transmitter that exploits spatial diversity, in accordance with the present invention.
- FIG. 5 is a block diagram of a fading channel model for a transmitter array, in accordance with the present invention.
- FIG. 6 is a block diagram of an equalizer embodying the principles of a multichannel implementation in accordance with the present invention.
- FIG. 7 is a block diagram of an equalizer embodying the principles of a multiuser implementation in accordance with the present invention.

DETAILED DESCRIPTION OF PREFERRED EMBODIMENTS

The present invention is directed to a novel equalizer system suitable for use in modern communication systems. A block-iterative configuration is employed to efficiently process received data, in a manner that is reliable and computationally inexpensive.

During each iteration, a set of tentative decisions are made as to the transmitted data, with the help of tentative decisions from a previous iteration. Reliability of the

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tentative decisions improves during each iteration, and therefore the tentative decisions from the previous iterations become more useful. The iterations continue until further iterations do not significantly change the set of tentative decisions.

Unlike conventional DFEs, the novel equalizers of the present invention suppress virtually all of the ISI, including precursor and postcursor ISI, in a nonlinear manner, significantly enhancing system performance. In fact, theory and simulations have demonstrated that the new class of equalizers can achieve a given level of reliability (as measured by bit-error rate) using significantly less received signal power than conventional equalizers. In particular, the new class of equalizers is capable of achieving a given bit error rate with 2.5 dB less received signal power as compared to a conventional system employing a minimum mean-square error DFE on severe ISI channels, even for uncoded systems.

As an example, FIG. 1 compares uncoded quadrature phase-shift keying (QPSK) performance predictions for the iterative equalizer of the present invention 20 with various classical equalizers, including the minimum mean-square error decision-feedback equalizer (MMSE-DFE) 22, the zero-forcing decision-feedback equalizer (ZF-DFE) 24, the minimum mean-square error linear equalizer (MMSE-LE) 26, and the zero-forcing linear equalizer (ZF-LE) 28, based on asymptotic (long ISI) analysis. The zero-forcing (ZF) technique selects the filters that completely eliminate ISI, usually at the expense of high noise enhancement. The minimum-mean-square-error (MMSE) technique, on the other hand, takes both into account, and attempts to minimize the combined effect of ISI and noise enhancement. It is noteworthy that the performance of the iterative equalizer of the present invention 20 approaches the additive white Gaussian channel bound. asypmtotically achieved by MLSD. When a channel introduces no intersymbol interference but only white Gaussian noise, equalization is not necessary. The receiver simply uses a symbol-by-symbol decision slicer and the bit-error rate achieved is known as the additive white Gaussian channel bound. When a channel introduces both intersymbol interference and white Gaussian noise, equalization is necessary. No matter what kind of equalization is used, one can never attain a lower bit-error rate than the additive white Gaussian channel bound at the same SNR. Receivers that use

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maximum-likelihood sequence detection (MLSD) are asymptotically optimal in the sense of minimizing bit-error rate at high SNR over channels that introduce intersymbol interference, and so it is expected that the bit-error rate in a receiver using MLSD approaches the additive white Gaussian channel bound at high SNR. In FIG. 1, the bit-error rate of the iterative equalizer of the present invention 20 approaches the additive white Gaussian channel bound at high SNR, which is asymptotically achieved by MLSD.

FIG. 2 is a block diagram of a receiver system 30 for receiving digital pulse amplitude modulation (PAM) carrier-modulated data signals transmitted from a far-end transmitter (not shown) over a communication channel.

In the transmitter, a stream of binary data is typically mapped into a sequence x[n] of complex numbers selected from a discrete symbol set. Multiple bits may be mapped into a single data symbol. The sequence of symbols is then combined with the transmit pulse shape $P_{TF}(t)$ to produce the complex-valued continuous-time signal

$$s(t) = \sum_{k} x[k] p_{TF}(t - kT),$$

where T is the modulation or baud interval. The real and imaginary components of s(t) amplitude-modulate in-phase and quadrature-phase carrier waves respectively, which combine to form the passband PAM signal transmitted to the receiver 30.

At the receiver 30, the received signal r(t) is applied to an input circuit comprising several components in series, the order of which may vary from system to system. Typically, the input circuit includes a bandpass filter 32 in order to suppress energy in the received signal r(t) outside the transmission frequency band. The filtered signal 33 is next processed by a phase splitter 34 that rejects negative frequencies and has two real-valued outputs representing a complex-valued signal. Alternatively, the functionality of the bandpass filter 32 and the phase splitter 34 can be replaced by an analytic passband filter, implemented using two physical filters, each with a real-valued output. An automatic gain control (AGC) 36 typically follows the passband filtering,

which compensates for slow amplitude variations in the received signal caused by the channel. A demodulator 38 multiplies the complex-valued signal 37 by $e^{j(\omega_c t + \theta(t))}$ where $\theta(t)$ is determined by a separate carrier recovery circuit 40 to compensate for the effects of frequency offset and phase jitter. In one embodiment, the complex multiply can be implemented using four real multiplies and two real additions. Finally, an analog-to-digital (A/D) converter 42 samples the two real-valued signals 39 representing the complex-valued signal. In high-performance communication systems, the incoming analog received signal may by oversampled so that sensitivity to timing and synchronization is reduced. A receiver capable of receiving and processing such oversampled signals is referred to in the art as a "fractionally-spaced" receiver. Thus, samples can be generated at an integer multiple of the baud rate, i.e., at a rate of Q/T complex samples/sec, where Q is a positive integer. The baud rate is usually estimated in the receiver 30 by a separate timing recovery circuit 48. In this manner, complex samples r[n] are produced, with the index n advancing at a rate of Q/T complex samples sec. The complex samples r[n] can be modelled as

$$r[n] = \sum_{k} x[k]a[n-k] + w[n]$$

where a[n] is the equivalent discrete-time baseband model of the PAM communication system and where w[n] is additive noise. The demodulator function 38 may optionally follow the sampler 42 to allow the demodulator 38 to be implemented in discrete time rather than continuous time. Additionally, both carrier recovery and timing recovery may be decision directed, in which case feedback from the output of the receiver is needed. The received samples r[n] are next passed to an equalizer system 44 configured in accordance with the present invention.

The equalizer 44 processes the received data r[n] in a block-iterative fashion. Specifically, during each iteration or "pass", a linear filter processes the received data, and tentative decisions made during a previous iteration are used to construct and subtract out an estimate of the ISI. The resulting ISI-reduced data is then passed on to a slicer,

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which generates a new set of tentative decisions. With each successive iteration, increasingly refined hard decisions (i.e., decisions made by the slicer 58 of FIG. 3 below) are generated using this strategy.

FIG. 3 is a block diagram of an iterated-decision equalizer 44 in accordance with a preferred embodiment of the present invention. The parameters of all systems and signals associated with the l th pass are denoted using the superscript "l". On the l th pass of the equalizer 44, where l=1,2,3..., the received data r[n] is first processed by first filter $b^l[n]$ 50, referred to herein as a "feed-forward filter" (preferably in the form of a linear filter), producing the sequence:

$$r^{\vee l}[n] = \sum_{k} b^{l}[kP]r[n-kP]$$

where P and Q are preferably prime integers with P < Q. (If a symbol-spaced equalizer is to be implemented, then P = Q = 1. If, however, a fractionally-spaced equalizer is to be implemented, then P = 1 and Q = 2 in most cases when there is not more than 100% excess bandwidth.) The output of a second filter, referred to herein as a "feed-back filter" $d^l[n]$ 52 and the input to a decision device, for example a slicer 58, are preferably both at the baud rate, so $r^l[n]$ 51 is downsampled at downsampler 54 by a factor of Q to provide downsampled signal $\tilde{r}^l[n]$ 55. Next, an appropriately constructed estimate $\hat{z}^l[n]$ 53 of the ISI is subtracted from downsampled signal $\tilde{r}^l[n]$ 55 at subtractor 56 to produce signal $\tilde{x}^l[n]$ 57, where

 $\widetilde{x}^{l}[n] = \widetilde{r}^{l}[n] - \hat{z}^{l}[n]$

and where

$$\hat{z}^{l}[n] = \sum_{k} d^{l}[k] \hat{x}^{l-1}[n-k].$$

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Since $\hat{z}^l[n]$ is intended as an ISI estimate, the zero-delay tap of the feedback filter $d^l[n]$ 52 is set at zero. The slicer 58 then generates the new set of hard decisions $\hat{x}^l[n]$ 59 from $\tilde{x}^l[n]$ using, for example, the well-known minimum-distance rule. Note that alternative functions are equally applicable to the present invention. For example, the subtractor 56 may comprise an adder or other means for combining the filtered signals from the feed-forward and feed-back filters 54, 52.

Further details of the invention depend on whether the channel is known at the receiver, partially known at the receiver, or unknown at the receiver. The known case determines the filter parameters using a fixed set of channel parameters, for example a known transfer function of a specific channel (wire, fiber optic, air, etc.). In the unknown case, the channel parameters are unknown, and the invention instead determines the filter parameters from the received data, using, for example, predetermined training symbols. In the partially-known case, an initial estimate of the channel parameters is used, and the iterative nature of the equalizer of the present invention is used to improve the channel estimate at the end of each pass. In each of the known, unknown and partially-known cases, during each pass, the feed-forward and feed-back filter parameters 50, 52 are modified.

Known Channel

When the channel is known at the receiver and P = Q = 1, one possibility is to select the feed-forward filter $b^l[n]$ 50 and the feed-back filter $d^l[n]$ 52 during the l th pass to maximize the signal-to-interference+noise ratio (SINR) at the slicer input, in which case the discrete-time Fourier transforms of the feed-forward filter $b^l[n]$ 50 and the feed-back filter $d^l[n]$ 52 are respectively

$$B^{l}(\omega) \propto \frac{A^{*}(\omega)}{N_{0} + \varepsilon_{s}(1 + (\rho_{x}^{l-1})^{2})|A(\omega)|^{2}}$$
$$D^{l}(\omega) = \rho_{x}^{l-1}(A(\omega)B^{l}(\omega) - E[A(\omega)B^{l}(\omega)])$$

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where $A(\omega)$ is the Fourier transform of the equivalent discrete-time baseband model of the PAM communication system, $\mathcal{E}_{\mathcal{S}}$ is the energy of the transmitted symbols, N_0 is the variance of the additive noise due to the channel, $E[\cdot]$ is the expectation operator (alternatively, the expectation $E[\cdot]$ can be replaced by a frequency average

 $<\cdot> = \frac{1}{2\pi} \int_{-\pi}^{\pi} d\omega$), and ρ_x^{l-1} is the normalized correlation between the transmitted data sequence x[n] 59 and the previous set of tentative decisions made on the previous iteration $\hat{x}^{l-1}[n]$ 60, i.e., $\rho_x^{l-1} \approx E[x*[n]\cdot\hat{x}^{l-1}[n]]/\varepsilon_s$. A possible method for computing the set of correlation coefficients ρ_x^l in the special case of M-PSK data symbols is the following iterative technique:

1. Set $\rho_x^0 = 0$ and let l = 1.

2. Compute the SINR γ^l at the slicer 58 input on the l th decoding pass from ρ_x^{l-1} via

$$\gamma^{l} = \left(\frac{1}{\xi^{l} e^{\xi^{l}} E_{1}(\xi^{l})} - 1\right) \cdot \frac{1}{1 - \left(\rho_{x}^{l-1}\right)^{2}}$$

where

$$E_1(s) = \int_s^\infty e^{-t} / t \ dt$$

and

$$\xi^{l} = \mathbf{N}_{0} \left[\varepsilon_{s} < \left| A(\omega) \right|^{2} > \left(1 - \left(\rho_{x}^{l-1} \right)^{2} \right) \right]^{-1}.$$

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Optionally, $\ensuremath{\gamma}^l$ can be computed (and in some cases more accurately) from $\ensuremath{\rho_x^{l-1}}$ via

$$\gamma^{l} = \frac{\varepsilon_{s} \Big| \langle A(\omega) B^{l}(\omega) \rangle \Big|^{2}}{\mathbf{N}_{0} < \Big| B^{l}(\omega) \Big|^{2} > + \varepsilon_{s} \Big(1 - (\rho_{x}^{l-1})^{2} \Big) < \Big(A(\omega) B^{l}(\omega) - \langle A(\omega) B^{l}(\omega) \rangle \Big)^{2} > }$$

3. Compute the normalized correlation coefficient ρ_x^l between the symbols x[n] and the decisions $\hat{x}^l[n]$ 59 generated at the slicer 58 via

$$\rho_x^l = 1 - 2\sin^2\left(\frac{\pi}{M}\right) 2Q \left(\sin\left(\frac{\pi}{M}\right) \sqrt{2\gamma^l}\right)$$

where

$$Q(v) = \frac{1}{\sqrt{2\pi}} \int_{v}^{\infty} e^{-\frac{t^2}{2}} dt$$

4. Increment l and go to step 2.

Partially Known Channel

When the channel is partially known at the receiver, the performance of the receiver may become particularly sensitive to the accuracy of the channel estimate used by the equalizer. To prevent such sensitivity, the equalizer can be modified to use an estimate of the channel impulse parameters with some degree of uncertainty as opposed to a fixed estimate of the channel parameters (as in the known-channel case). When P=Q=1, one possibility is to use the techniques for the known-channel case described above, except that the fixed channel $A(\omega)$ is replaced by the channel estimate $\hat{A}(\omega)$, and the

noise variance N_0 is replaced by $N_0 + \varepsilon_s < \left| A(\omega) - \hat{A}(\omega)^2 \right| >$. Furthermore, it is possible to exploit the iterative nature of the equalizer to improve the channel estimate at the end of each pass, thus leading to a higher SINR in the slicer 58, which in turn lowers the probability of symbol error. At the end of the l th pass, the equalizer has knowledge of the received sequence r[n] and the decisions $\hat{x}^l[n]$ of the current pass, for $n = 0, 1, \ldots, N-1$. For the purpose of estimating the channel impulse response a[n], it may be assumed that r[n] and $\hat{x}^l[n]$ are zero prior to n=0 and after n = N-1. Furthermore, for illustrative purposes, it is assumed here that a[n] is nonzero for $n = 0, 1, \ldots, N-1$. If two vectors are defined

$$\hat{\mathbf{a}}^l = \left[\hat{a}^l[0]\hat{a}^l[1]...\hat{a}^l[l-1]\right]^*$$

$$\hat{\mathbf{x}}^{l}[n] = \left[\hat{x}^{l}[n]\hat{x}^{l}[n-1]...\hat{x}^{l}[n-L+1]\right]^{T}$$

(where the superscript "*" denotes the conjugate-transpose operation for vectors and the conjugation operation for scalars, and where the superscript "T" denotes transposition), then the difference between the received data symbols and the response of the channel to the most recent set of decisions; i.e.,

$$\hat{\varepsilon}(\hat{\mathbf{a}}^l) = \sum_{n=-\infty}^{\infty} \left| r[n] - \hat{\mathbf{a}}^{l^*} \hat{\mathbf{x}}^l[n] \right|^2,$$

is minimized when

$$\hat{\mathbf{a}}_{opt}^l = \left[\Phi^l \right]^{-1} \mathbf{u}^l,$$

where

$$\Phi^{l} = \sum_{n=-\infty}^{\infty} \hat{\mathbf{x}}^{l}[n]\hat{\mathbf{x}}^{l^{*}}[n]$$

and

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$$\mathbf{u}^{l} = \sum_{n=-\infty}^{\infty} \mathbf{r} * [n] \hat{\mathbf{x}}^{l} [n].$$

For M-PSK symbols, the variance of the error in the channel estimate $e^{l}[n] = a[n] - \hat{a}^{l}[n]$ made at the end of the l th pass is approximately

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$$\left(\sigma_e^l\right)^2 \approx \frac{2 < \left|A(\omega)\right|^2 > \varepsilon_s (1 - \rho_x^l) + N_0}{N\varepsilon_s}$$

If, at the l th pass, the variance $\left(\sigma_e^{l-1}\right)^2$ of the channel estimate made at the end of the previous pass is smaller than the variance of the channel estimate used for the (l-1) th

pass, then the former should be used. If, on the other hand, the variance $\left(\sigma_e^{l-1}\right)^2$ is larger than the variance of the channel estimate used for the (*l*-1) th pass, then the estimate used for the (*l*-1) th pass is used again for the *l* th pass.

Unknown Channel

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When the channel is unknown at the receiver, an adaptive version of the present invention is required, in which optimal FIR filter coefficients are selected automatically (from the received data) without explicit knowledge of the channel characteristics.

The multipass equalizer of the present invention is designed to process received data in a block-iterative fashion, so it is ideally suited for packet communication in which the packet size is chosen small enough that the channel encountered by each packet appears linear time-invariant. As is typically the case with other adaptive equalizers, the adaptive iterated-decision equalizer makes use of training symbols sent along in the packet with the data symbols. Suppose that a block of white M -ary PSK symbols x[n] for n=0,1,...N-1 is transmitted; some of the symbols (not necessarily at the head of the packet) are for training, while the rest are data symbols.

The feed-forward filter $b^l[n]$ 50 and the feed-back filter $d^l[n]$ 52 for the l th iteration preferably comprise finite-length filters. Specifically, the feed-forward filter $b^l[n]$ 50 has J_1 strictly anticausal taps and J_2 strictly causal taps plus a center tap, while the feed-back filter $d^l[n]$ 52 has K_1 strictly anticausal taps and K_2 strictly causal taps with no center tap.

Prior to the first pass (l=1), the hard decisions $\hat{x}^0[n]$ are initialized. Since the locations and values of the training symbols in x[n] are known at the receiver, the hard decisions are initialized as $\hat{x}^0[n] = x[n]$ for the n corresponding to those locations. For all the other n between 0 and N-1 inclusive, $\hat{x}^0[n]$ is set to a "neutral" value; as an example, for white PSK symbols, this value should be zero.

On the l th pass of the equalizer where $l=1,\,2,\,3,\,\ldots$ the slicer 58 input $\widetilde{x}^l[n]$ can be expressed as

$$\widetilde{x}^{l}[n] = c^{l^*}q^{l}[n]$$

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where

$$\mathbf{c}^{l} = \begin{bmatrix} \mathbf{b}^{l} \\ -\mathbf{d}^{l} \end{bmatrix} \qquad \mathbf{q}^{l}[n] = \begin{bmatrix} \mathbf{r}[n] \\ \hat{\mathbf{x}}^{l-1}[n] \end{bmatrix}$$

and

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Using, for example, a minimum-distance rule, the slicer 58 next generates the hard decisions $\hat{x}^l[n]$ from $\tilde{x}^l[n]$ for all n between 0 and N-1 inclusive, except for those n corresponding to the locations of training symbols in x[n]. For those n, the hard decisions are set to $\hat{x}^l[n] = x[n]$.

During the l th iteration, there are two sets of data available to the receiver: the received data r[n] and tentative decisions from the previous iteration $\hat{x}^{l-1}[n]$,

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n=0,1,...,N-1. If it is assumed that $x[n] \approx \hat{x}^{l-1}[n]$ for the purpose of determining the optimal filters (as is similarly done in the adaptive DFE in decision-directed mode), then it is reasonable to choose the feed-forward filter parameters $b^l[n]$ 50 and the feedback filter parameters $d^l[n]$ 52 so as to minimize the sum of error squares:

$$\varepsilon(c^l) = \sum_{n=-\infty}^{\infty} \left| \hat{x}^{l-1}[n] - c^{l^*} q^l[n] \right|^2.$$

Since this is a linear least-squares estimation problem, the optimum c^l is represented by:

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$$\mathbf{c}_{opt}^{l} = \left[\Phi^{l}\right]^{-1} \mathbf{u}^{l},$$

where

$$\Phi^l = \sum_{n=-\infty}^{\infty} q^l [n] q^{l^*} [n]$$

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and

$$u^{l} = \sum_{n=-\infty}^{\infty} \hat{x}^{l-1*}[n]q^{l}[n].$$

The resulting equalizer lends itself readily to practical implementation, even for large filter lengths. In particular, the matrix Φ^l can be efficiently computed using correlation functions involving r[n] and $\hat{x}^{l-1}[n]$. Specifically, if the following deterministic correlation functions are defined as:

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$$\beta[n_1, n_2] = \sum_{k = -\infty}^{\infty} r[kQ - n_1] r^* [kQ - n_2]$$

$$\theta^l[n] = \sum_{k = -\infty}^{\infty} \hat{y}^{l-1} [k] \hat{y}^{l-1}^* [k-n]$$

$$\psi^l[n] = \sum_{k = -\infty}^{\infty} r[k] \hat{y}^{l-1}^* [k-n],$$

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where

$$\hat{y}^{l-1}[n] = \begin{cases} \hat{x}^{l-1}[n/Q], & n = 0, Q, 2Q, ..., (N-1)Q \\ 0, & \text{otherwise} \end{cases}$$

then Φ^l can be expressed as

$$\Phi^l = \begin{bmatrix} \beta & \Psi^l \\ {\Psi^l}^* & \Theta^l \end{bmatrix}$$

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where

$$\beta = \begin{bmatrix} \beta[-J_{1}P, -J_{1}P] & \beta[-J_{1}P, -(J_{1}-1)P] & \cdots & \beta[-J_{1}P, J_{2}P] \\ \beta[-(J_{1}-1)P, -J_{1}P] & \beta[-(J_{1}-1)P, -(J_{1}-1)P] & \cdots & \beta[-(J_{1}-1)P, J_{2}P] \\ & & \ddots & \\ \beta[J_{2}P, -J_{1}P] & \beta[J_{2}P, -(J_{1}-1)P] & \cdots & \beta[J_{2}P, J_{2}P] \end{bmatrix}$$

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$$\Theta^{l} = \begin{bmatrix} \theta^{l}[0] & \theta^{l}[Q] & \cdots & \theta^{l}[(K_{1}-1)Q] & \theta^{l}[(K_{1}+1)Q] & \cdots & \theta^{l}[(K_{1}+K_{2})Q] \\ \theta^{l}[-Q] & \theta^{l}[0] & \cdots & \theta^{l}[(K_{1}-2)Q] & \theta^{l}[K_{1}Q] & \cdots & \theta^{l}[(K_{1}+K_{2}-1)Q] \\ \vdots & \vdots & \ddots & \vdots & \vdots & \ddots & \vdots \\ \theta^{l}[-(K_{1}-1)Q] & \theta^{l}[-(K_{1}-2)Q] & \cdots & \theta^{l}[0] & \theta^{l}[2Q] & \cdots & \theta^{l}[(K_{2}+1)Q] \\ \theta^{l}[-(K_{1}+1)Q] & \theta^{l}[-K_{1}Q] & \cdots & \theta^{l}[-2Q] & \theta^{l}[0] & \cdots & \theta^{l}[(K_{2}-1)Q] \\ \vdots & \vdots & \ddots & \vdots & \vdots & \ddots & \vdots \\ \theta^{l}[-(K_{1}+K_{2})Q] & \theta^{l}[-(K_{1}+K_{2}-1)Q] & \cdots & \theta^{l}[-(K_{2}+1)Q] & \theta^{l}[-(K_{2}-1)Q] & \cdots & \theta^{l}[0] \end{bmatrix}$$

and

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$$\Psi^{l} = \begin{bmatrix} \psi^{l}[J_{1}P - K_{1}Q] & \psi^{l}[J_{1}P - (K_{1} - 1)Q] & \cdots & \psi^{l}[J_{1}P - Q] & \psi^{l}[J_{1}P + Q] & \cdots & \psi^{l}[J_{1}P + K_{2}Q] \\ \psi^{l}[(J_{1} - 1)P - K_{1}Q] & \psi^{l}[(J_{1} - 1)P - (K_{1} - 1)Q] & \cdots & \psi^{l}[(J_{1} - 1)P - Q] & \psi^{l}[(J_{1} - 1)P + Q] & \cdots & \psi^{l}[(J_{1} - 1)P + K_{2}Q] \\ \vdots & \vdots & \ddots & \vdots & \ddots & \vdots \\ \psi^{l}[-J_{2}P - K_{1}Q] & \psi^{l}[-J_{2}P - (K_{1} - 1)Q] & \cdots & \psi^{l}[-J_{2}P - Q] & \psi^{l}[-J_{2}P + Q] & \cdots & \psi^{l}[-J_{2}P + K_{2}Q] \end{bmatrix}.$$

Note that in general, none of the matrices β , Θ^I , or Ψ^I is Toeplitz. However, Θ^I is a $(K_1+K_2+1)\times (K_1+K_2+1)$ Toeplitz matrix with its (K_1+1) th row and (K_1+1) th column deleted, and β exhibits cyclostationary properties with each diagonal having a period of Q. Moreover, depending on the values of P and Q, the matrix Ψ^I may have a significant number of entries that have the same value. Thus, the special structure in Φ^I can be exploited so that the redundant computation of repeated entries is avoided. Furthermore, since

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 Φ^I is a Hermitian matrix, it is only necessary to compute half of the distinct entries. In the special case of P=Q=1, β is a Toeplitz matrix, and Ψ^I can be thought of as originating from a $(J_1+J_2+1)\times (K_1+K_2+1)$ matrix with the elements on each diagonal being equal, but now with the (K_1+1) th column deleted. In this special case, the computation of Φ^I requires only $2(J_1+J_2+K_1+K_2+1)$ of the $(J_1+J_2+K_1+K_2+1)^2$ entries, as there are only that many distinct entries. If the total number of taps in the equalizer is 100, then the computation of Φ^I has been made approximately 50 times more efficient! The computation of Φ^I can be made more efficient. Specifically, since Φ^I can be expressed as a partitioned matrix in which β and Θ^I are square, Φ^I can be partitioned into

$$\begin{bmatrix} \Phi^l \end{bmatrix}^{-1} = \begin{bmatrix} \widetilde{\beta}^l & \widetilde{\Psi}^l \\ \widetilde{\Psi}^{l^*} & \widetilde{\Theta}^l \end{bmatrix}$$

where $\widetilde{\beta}^l$, $\widetilde{\Theta}^l$, and $\widetilde{\Psi}^l$ have the same sizes as β , Θ^l , and Ψ^l respectively. Note that this takes advantage of the fact that the inverse of a Hermitian matrix is also Hermitian. Then, the matrices $\widetilde{\beta}^l$, $\widetilde{\Theta}^l$, and $\widetilde{\Psi}^l$ can be computed either by the formulas

$$\begin{split} \tilde{\beta}^{l} &= (\beta - \Psi^{l} \{ \Theta^{l} \}^{-1} \Psi^{l^{\dagger}})^{-1} \\ \tilde{\Psi}^{l} &= -(\beta - \Psi^{l} \{ \Theta^{l} \}^{-1} \Psi^{l^{\dagger}})^{-1} \cdot (\Psi^{l} \{ \Theta^{l} \}^{-1}) \\ \tilde{\Theta}^{l} &= [\Theta^{l}]^{-1} + (\Psi^{l} \{ \Theta^{l} \}^{-1})^{\dagger} \cdot (\beta - \Psi^{l} \{ \Theta^{l} \}^{-1} \Psi^{l^{\dagger}})^{-1} \cdot (\Psi^{l} \{ \Theta^{l} \}^{-1}) \end{split}$$

or by the equivalent formulas

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$$\tilde{\beta}^{l} = \beta^{-1} + (\beta^{-1}\Psi^{l}) \cdot (\Theta^{l} - \Psi^{l\dagger}\beta^{-1}\Psi^{l})^{-1} \cdot (\beta^{-1}\Psi^{l})^{\dagger}$$

$$\tilde{\Psi}^{l} = -(\beta^{-1}\Psi^{l}) \cdot (\Theta^{l} - \Psi^{l\dagger}\beta^{-1}\Psi^{l})^{-1}$$

$$\tilde{\Theta}^{l} = (\Theta^{l} - \Psi^{l\dagger}\beta^{-1}\Psi^{l})^{-1}.$$

(See H. Lütkepohl, *Handbook of Matrices*. Chichester, England: Wiley, 1996.) The factors within parentheses in the two above sets of equations indicate quantities that need to be computed only once for a given *l*. So if the first set of formulas is used, then

$$[\Theta^l]^{-1}, \ \Psi^l[\Theta^l]^{-1}, \quad (\beta - \Psi^l[\Theta^l]^{-1}\Psi^{l^\dagger})^{-1}, \ \text{and} \ (\beta - \Psi^l[\Theta^l]^{-1}\Psi^{l^\dagger})^{-1} \cdot (\Psi^l[\Theta^l]^{-1})$$

should be computed in that order during each iteration. Similarly, if the second set is used, then

$$\beta^{-1}$$
, $(\beta^{-1}\Psi^l)$, $(\Theta^l - \Psi^{l\dagger}\beta^{-1}\Psi^l)^{-1}$, and $(\beta^{-1}\Psi^l) \cdot (\Theta^l - \Psi^{l\dagger}\beta^{-1}\Psi^l)^{-1}$

should be computed in that order during each iteration. In either case, the task of computing $\left[\Phi^I\right]^{-1}$ has been modified into a task involving the computation of an inverse matrix the size of β and another inverse matrix the size of Θ^I . One advantage of using the second set of formulas is that the same β^{-1} is used during all iterations, so it only needs to be computed once during the whole multipass process. Thus, the only matrix inverse that needs to be computed during each iteration (except for the first iteration) is

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$$(\Theta^l - \Psi^{l^*}\beta^{-1}\Psi^l)^{-1}$$
.

and the original task of computing the inverse of a $(J_1+J_2+K_1+K_2+1)\times(J_1+J_2+K_1+K_2+1)\ \text{matrix has been converted into a task involving}$ the computation of the inverse of a $(K_1+K_2)\times(K_1+K_2)$ matrix. If J_1 is large, and $J_1=J_2=K_1=K_2\ ,$ then a reduction of computation by an order of magnitude is plausible. We now turn to a couple of implementation issues. First, when P=Q=1, the finite-length adaptive filters ideally approximate

$$B^{l}(\omega) \propto \frac{A^{*}(\omega)}{N_{0} + \varepsilon_{s}(1 + (\rho_{x}^{l-1})^{2})|A(\omega)|^{2}}$$
$$D^{l}(\omega) = \rho_{x}^{l-1} \Big(A(\omega)B^{l}(\omega) - E\Big[A(\omega)B^{l}(\omega)\Big]\Big)$$

which are infinite in length. The optimal feed-forward filter $b^l[n]$ includes a filter matched to a[n], and the optimal $d^l[n]$ includes a cascade of a[n] and the corresponding matched filter, suggesting that a reasonable rule of thumb is to select J_1, J_2, K_1, K_2 such that each FIR filter covers twice the span of the channel impulse response. Second, the block-iterative nature of the equalizer allows the training symbols to be located anywhere in the packet. Since (in contrast to the DFE) the locations do not appear to affect equalizer performance, the training symbols are arbitrarily chosen to have uniform spacing within each packet.

Error-Control Coding

In most communication systems, it is preferred to have the ability to apply error-control coding (ECC) to the data prior to modulation and transmission. Examples of error-control codes include block codes, convolutional codes, and trellis-coded modulation. The present invention includes two implementations of the equalizer that allow such coding to be explicitly

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taken into account at the receiver. In one version, equalization and error-correction decoding are performed separately. This implementation is obtained by processing the final decisions of the equalizer using a decoder placed after the equalizer in FIG. 2. In the other version, equalization and error-correction decoding are performed jointly, leading to much improved performance. This implementation is obtained by generalizing the iterative equalizer of FIG. 3 by coding the symbols $\hat{x}^{l-1}[n]$ using an error-correction encoder before being filtered in the feedback loop by $d^l[n]$, and by replacing the slicer depicted in FIG. 3 with an appropriate soft- or hard-decision error-correction decoder. For example, if trellis coding (see (a) G. Ungerboeck, "Channel coding with multilevel/phase signals," IEEE Trans. Inform. Theory, vol. IT-28, pp. 55--67, Jan. 1982. (b) G. Ungerboeck, "Trellis-coded modulation with redundant signal sets Part I: Introduction," IEEE Commun. Mag., vol. 25, pp. 5--11, Feb. 1987. (c) G. Ungerboeck, "Trellis-coded modulation with redundant signal sets Part II: State of the art," IEEE Commun. Mag., vol. 25, pp. 12--21, Feb. 1987.) were employed, a classical soft-decision Viterbi decoder could be used in the resulting receiver. As another example, if turbo-type codes (see (a) C. Berrou, A. Glavieux, and P. Thitimajshima, "Near Shannon limit error-correcting coding and decoding: Turbo codes," in *Proc. Int. Conf.* Commun., pp. 1064--1070, 1993. (b) C. Berrou and A. Glavieux, "Near optimum error correcting coding and decoding: turbo codes," IEEE Trans. Commun., vol. 44, pp. 1261-1271, Oct. 1996.) were used in conjunction with this equalizer, a doubly-iterative composite receiver could be used in which iterative turbo-decoding takes place in lieu of the slicer as a nested subsystem of the larger iterative equalizer structure.

Implementation for Transmitter Antenna Arrays

Many communication systems, especially wireless ones, use a transmitter antenna array, whereby the data to be transmitted are sent from each of a set of different antenna elements. One possible transmitter structure which uses m antennas to exploit spatial diversity is depicted in FIG. 4 (see G. W. Wornell and M. D. Trott, "Efficient signal processing techniques for exploiting transmit antenna diversity on fading channels," *IEEE Trans. Signal Process.*, vol. 45, pp. 191--205, Jan. 1997). For convenience the focus of the present

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discussion is on equivalent discrete-time baseband models. The symbol stream x[n] 72 is scaled down by a factor of \sqrt{m} at scaler 74 and then processed by a linear precoder 76 at each of the constituent antennas of the transmitter to produce the transmitted symbol streams $v_1[n], v_2[n], ..., v_m[n]$ 78.

The present invention includes an implementation of the block-iterative equalizer that is designed for use with a transmitter antenna array like the one described above. If the channel can be described by a nonselective fading model, shown in FIG. 5, then the signal at the receiver 82 can be modelled as

$$r[n] = \sum_{i=1}^{m} a_i v_i[n] + w[n]$$

where w[n] 84 is additive noise and $a_1, a_2, ..., a_m$ 80 are fading coefficients. Alternatively, the received signal can be expressed in the form

$$r[n] = \sum_{k} x[k] a[n-k] + w[n]$$

where

$$a[n] = \frac{1}{\sqrt{m}} \sum_{i=1}^{m} a_i h_i[n]$$

is the impulse response of the "effective" channel generated by the antenna precoder. The channel has a frequency response of

$$A(\omega) = \frac{1}{\sqrt{m}} \sum_{i=1}^{m} a_i H_i(\omega),$$

so the antenna precoding effectively transforms the original nonselective fading channel into a frequency-selective fading channel, and the implementation of the block-iterative equalizer

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corresponding to FIG. 3 and described above is applicable. As discussed above, specific details of the implementation depend on whether the "effective" channel is known, partially known, or unknown.

Multichannel Implementation

In many communication systems, especially wireless ones, multiple copies of the transmitted waveform are received, each being distorted differently. Such is the case, for example, when the transmission is received by a multiple element array. In such cases, it is common to use an equalizer with what is referred to as a multichannel implementation for jointly processing the received streams. For such applications, the present invention includes an efficient multichannel implementation of the block-iterative equalizer, which can enhance performance over the single channel implementation.

The structure of a multichannel, fractionally spaced iterated-decision equalizer is depicted in FIG. 6. For each of the m channels, the equalizer receives a signal that is typically modelled as

$$r_i[n] = \sum_k x[k] a_i[n-k] + w_i[n]$$
 for $i = 1, 2, ..., m$

where x[n] is the sequence of transmitted symbols, $a_i[n]$ is the equivalent discrete-time baseband model of the i th channel, and $w_i[n]$ is the additive noise of the i th channel. Therefore, in this embodiment, the main difference in the equalizer is that instead of one fractionally-spaced feed-forward filter, there are now m such filters 90. The operation of the equalizer remains essentially the same, except that the output of the feedback branch 92 is now subtracted from the sum of the feedforward branch outputs, rather than from just a single feedforward branch output.

If the multiple channels are unknown at the receiver, the technique employed in the single channel case can be generalized. Thus, the slicer input $\tilde{x}^{l}[n]$ can be expressed in

vector form as

$$\widetilde{x}^{l}[n] = c^{l^*} q^{l}[n]$$

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$$\mathbf{c}^{l} = \begin{bmatrix} \mathbf{b}_{1}^{l} \\ \mathbf{b}_{2}^{l} \\ \vdots \\ \mathbf{b}_{m}^{l} \\ -\mathbf{d}^{l} \end{bmatrix} \qquad \mathbf{q}^{l}[n] = \begin{bmatrix} \mathbf{r}_{1}[n] \\ \mathbf{r}_{2}[n] \\ \vdots \\ \mathbf{r}_{m}[n] \\ \hat{\mathbf{x}}^{l-1}[n] \end{bmatrix}$$

and

If $b_1^l[n], b_2^l[n], \dots, b_m^l[n]$, and $d^l[n]$ are chosen so as to minimize the sum of error squares:

$$\varepsilon(c^l) = \sum_{n=-\infty}^{\infty} \left| \hat{x}^{l-1}[n] - c^{l^*} q^l[n] \right|^2$$

then the optimum c^l is

$$\mathbf{c}_{opt}^{l} = \left[\Phi^{l}\right]^{-1} \mathbf{u}^{l},$$

where

$$\Phi^l = \sum_{n=-\infty}^{\infty} q^l [n] q^{l^*} [n]$$

and

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$$\mathbf{u}^{l} = \sum_{n=-\infty}^{\infty} \hat{x}^{l-1*}[n]\mathbf{q}^{l}[n].$$

The computation of the matrix Φ^l can be made more efficient by first defining the following deterministic correlation functions

$$\beta_{ij}[n_1, n_2] = \sum_{k=-\infty}^{\infty} r_i [kQ - n_1] r_j^* [kQ - n_2]$$

$$\theta^l[n] = \sum_{k=-\infty}^{\infty} \hat{y}^{l-1}[k] \hat{y}^{l-1^*}[k-n]$$

$$\psi_i^l[n] = \sum_{k=-\infty}^{\infty} r_i [k] \hat{y}^{l-1^*}[k-n],$$

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$$\hat{y}^{l-1}[n] = \begin{cases} \hat{x}^{l-1}[n/Q], & n = 0, Q, 2Q, ..., (N-1)Q \\ 0, & \text{otherwise} \end{cases}$$

Thus, for m channels, the matrix Φ^l may be rewritten as

$$\Phi^{l} = \begin{bmatrix}
\beta_{11} & \beta_{12} & \cdots & \beta_{1m} & \Psi_{1}^{l} \\
\beta_{12}^{\dagger} & \beta_{22} & \cdots & \beta_{2m} & \Psi_{2}^{l} \\
\vdots & \vdots & \ddots & \vdots & \vdots \\
\beta_{1m}^{\dagger} & \beta_{2m}^{\dagger} & \cdots & \beta_{mm} & \Psi_{m}^{l} \\
\Psi_{1}^{l \dagger} & \Psi_{2}^{l \dagger} & \cdots & \Psi_{m}^{l \dagger} & \Theta^{l}
\end{bmatrix}$$

where

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$$\beta_{ij} = \begin{bmatrix} \beta_{ij}[-J_1P, -J_1P] & \beta_{ij}[-J_1P, -(J_1-1)P] & \cdots & \beta_{ij}[-J_1P, J_2P] \\ \beta_{ij}[-(J_1-1)P, -J_1P] & \beta_{ij}[-(J_1-1)P, -(J_1-1)P] & \cdots & \beta_{ij}[-(J_1-1)P, J_2P] \\ \vdots & \vdots & \ddots & \vdots \\ \beta_{ij}[J_2P, -J_1P] & \beta_{ij}[J_2P, -(J_1-1)P] & \cdots & \beta_{ij}[J_2P, J_2P] \end{bmatrix}$$

$$\beta_{ij} = \begin{bmatrix} \beta_{ij}[-J_{1}P, -J_{1}P] & \beta_{ij}[-J_{1}P, -(J_{1}-1)P] & \cdots & \beta_{ij}[-J_{1}P, J_{2}P] \\ \beta_{ij}[-(J_{1}-1)P, -J_{1}P] & \beta_{ij}[-(J_{1}-1)P, -(J_{1}-1)P] & \cdots & \beta_{ij}[-(J_{1}-1)P, J_{2}P] \\ \vdots & \vdots & \ddots & \vdots \\ \beta_{ij}[J_{2}P, -J_{1}P] & \beta_{ij}[J_{2}P, -(J_{1}-1)P] & \cdots & \beta_{ij}[J_{2}P, J_{2}P] \end{bmatrix}$$

$$\theta^{l}[0] & \theta^{l}[Q] & \cdots & \theta^{l}[(K_{1}-1)Q] & \theta^{l}[(K_{1}+1)Q] & \cdots & \theta^{l}[(K_{1}+K_{2})Q] \\ \theta^{l}[-Q] & \theta^{l}[0] & \cdots & \theta^{l}[(K_{1}-2)Q] & \theta^{l}[K_{1}Q] & \cdots & \theta^{l}[(K_{1}+K_{2}-1)Q] \\ \vdots & \vdots & \ddots & \vdots & \ddots & \vdots \\ \theta^{l}[-(K_{1}-1)Q] & \theta^{l}[-(K_{1}-2)Q] & \cdots & \theta^{l}[0] & \theta^{l}[2Q] & \cdots & \theta^{l}[(K_{2}-1)Q] \\ \theta^{l}[-(K_{1}+1)Q] & \theta^{l}[-(K_{1}-2)Q] & \cdots & \theta^{l}[-2Q] & \theta^{l}[0] & \cdots & \theta^{l}[(K_{2}-1)Q] \\ \vdots & \vdots & \ddots & \vdots & \ddots & \vdots \\ \theta^{l}[-(K_{1}+K_{2})Q] & \theta^{l}[-(K_{1}+K_{2}-1)Q] & \cdots & \theta^{l}[-(K_{2}+1)Q] & \theta^{l}[-(K_{2}-1)Q] & \cdots & \theta^{l}[0] \end{bmatrix}$$

and 25

$$\Psi_{i}^{l} = \begin{bmatrix} \psi_{i}^{l}[J_{1}P-K_{1}Q] & \psi_{i}^{l}[J_{1}P-(K_{1}-1)Q] & \cdots & \psi_{i}^{l}[J_{1}P-Q] & \psi_{i}^{l}[J_{1}P+Q] & \cdots & \psi_{i}^{l}[J_{1}P+K_{2}Q] \\ \psi_{i}^{l}[(J_{1}-1)P-K_{1}Q] & \psi_{i}^{l}[(J_{1}-1)P-(K_{1}-1)Q] & \cdots & \psi_{i}^{l}[(J_{1}-1)P-Q] & \psi_{i}^{l}[(J_{1}-1)P+Q] & \cdots & \psi_{i}^{l}[(J_{1}-1)P+K_{2}Q] \\ \vdots & \vdots & \ddots & \vdots \\ \vdots & \vdots & \ddots & \vdots \\ \psi_{i}^{l}[-J_{2}P-K_{1}Q] & \psi_{i}^{l}[-J_{2}P-(K_{1}-1)Q] & \psi_{i}^{l}[-J_{2}P-Q] & \psi_{i}^{l}[-J_{2}P+Q] & \cdots & \psi_{i}^{l}[-J_{2}P+K_{2}Q] \end{bmatrix}.$$

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Note that in general, none of the matrices β_{ij} , Θ^l , or Ψ_i^l is Toeplitz. However, Θ^l is a $(K_1+K_2+1)\times (K_1+K_2+1)$ Toeplitz matrix with its (K_1+1) th row and (K_1+1) th column deleted, and β_{ij} exhibits cyclostationary properties with each diagonal having a period of Q. Moreover, depending on the values of P and Q, the matrix Ψ_i^l may have a significant number of entries that have the same value. Thus, the special structure in Φ^l can be exploited so that the redundant computation of repeated entries is avoided. Furthermore, since Φ^l is a Hermitian matrix, it is only necessary to compute half of the distinct entries.

As in the single-channel case, the computation of $\left[\Phi^{l}\right]^{-1}$ for the multichannel equalizer can be made more efficient by partitioning Φ^{l} into four submatrices such that the upper left and lower right submatrices are square, and then applying the formulas for the inverse of a partitioned matrix. Since these formulas involve computing the inverses of two matrices with the same sizes as the upper left and lower right submatrices, it is usually best to partition Φ^{l} into submatrices of approximately the same size so that computation of a large matrix inverse is avoided.

Multiuser Implementation

The present invention further comprises a block-iterative multiuser equalizer that jointly takes into account multiple-access, co-channel, and extra-network interference, which are generally experienced in communication networks, especially wireless networks. For multiple-access interference in particular, the invention jointly suppresses intersymbol and multiple-access interference in a manner generalizing the structure of FIG. 3.

The signal at the receiver is typically modelled as

$$r[n] = \sum_{i=1}^{p} \sum_{k} x_{i}[k] a_{i}[n-k] + w[n]$$

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where p is the number of users, $x_i[n]$ is the sequence of symbols transmitted by the i th user, $a_i[n]$ is the equivalent discrete-time baseband model of the channel encountered by the ith user, and w[n] represents additive noise and interference.

The multiuser, fractionally spaced iterated-decision equalizer consists of p main branches, with each branch designed to decode the symbol stream of a different user. The structure of the i th branch, shown in FIG. 7, is similar to the single-user equalizer shown in FIG. 3 except that the present embodiment includes an additional p-1 feedback loops 94 to cancel multiple-access interference caused by the other p-1 users. Because each branch requires access to the tentative decisions made in the previous iteration for all p users (i.e., each branch requires access to $\hat{x}_1^{l-1}[n], \hat{x}_2^{l-1}[n], ..., \hat{x}_p^{l-1}[n]$), all branches must complete the (l-1) th iteration before any branch proceeds to the l th iteration.

If the channels are unknown at the receiver, the approach for the single-user case can be generalized for the multiple-user case. Thus, for the i th user, the slicer input $\widetilde{x}_i^l[n]$ can be expressed in vector form as

$$\widetilde{x}_{i}^{l}[n] = c_{i}^{l*} q_{i}^{l}[n]$$

where

$$\mathbf{c}_{i}^{l} = \begin{bmatrix} \mathbf{b}_{i}^{l} \\ -\mathbf{d}_{1i}^{l} \\ \vdots \\ -\mathbf{d}_{ii}^{l} \\ \vdots \\ -\mathbf{d}_{pi}^{l} \end{bmatrix} \qquad \mathbf{q}_{i}^{l}[n] = \begin{bmatrix} \mathbf{r}[n] \\ \hat{\mathbf{x}}_{1}^{l-1}[n] \\ \vdots \\ \hat{\mathbf{x}}_{i}^{l-1}[n] \\ \vdots \\ \hat{\mathbf{x}}_{p}^{l-1}[n] \end{bmatrix}$$

and

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$$b_{i}^{l} = \begin{bmatrix} b_{i}^{l}[-J_{1}P] & \cdots & b_{i}^{l}[-P] & b_{i}^{l}[0] & b_{i}^{l}[P] & \cdots & b_{i}^{l}[J_{2}P] \end{bmatrix}^{\bigstar}$$

$$d_{ji}^{l} = \begin{cases} \begin{bmatrix} d_{ji}^{l}[-K_{1}] & \cdots & d_{ji}^{l}[-1] & d_{ji}^{l}[1] & \cdots & d_{ji}^{l}[K_{2}] \end{bmatrix}^{\bigstar}, & \text{if } j = i \\ \begin{bmatrix} d_{ji}^{l}[-K_{1}] & \cdots & d_{ji}^{l}[-1] & d_{ji}^{l}[0] & d_{ji}^{l}[1] & \cdots & d_{ji}^{l}[K_{2}] \end{bmatrix}^{\bigstar}, & \text{if } j \neq i \end{cases}$$

$$r[n] = \begin{bmatrix} r[nQ + J_{1}P] & \cdots & r[nQ + P] & r[nQ] & r[nQ - P] & \cdots & r[nQ - J_{2}P] \end{bmatrix}^{T}$$

$$\hat{x}_{j}^{l-1}[n] = \begin{cases} \begin{bmatrix} \hat{x}_{j}^{l-1}[n+K_{1}] & \cdots & \hat{x}_{j}^{l-1}[n+1] & \hat{x}_{j}^{l-1}[n-1] & \cdots & \hat{x}_{j}^{l-1}[n-K_{2}] \end{bmatrix}^{T}, & \text{if } j = i \\ \\ \begin{bmatrix} \hat{x}_{j}^{l-1}[n+K_{1}] & \cdots & \hat{x}_{j}^{l-1}[n+1] & \hat{x}_{j}^{l-1}[n] & \hat{x}_{j}^{l-1}[n-1] & \cdots & \hat{x}_{j}^{l-1}[n-K_{2}] \end{bmatrix}^{T}, & \text{if } j \neq i \end{cases}$$

where j = 1, 2, ..., p. If $b_i^l[n], d_{1i}^l[n], d_{2i}^l[n], ..., d_{pi}^l[n]$ are chosen so as to minimize the sum of error squares:

$$\varepsilon(c_i^l) = \sum_{n=-\infty}^{\infty} \left| \hat{x}_i^{l-1}[n] - c_i^{l*} q_i^l[n] \right|^2,$$

then the optimum c_i^l for the *i* th user is

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$$\mathbf{c}_{i,opt}^l = \left[\Phi_i^l\right]^{-1} \mathbf{u}_i^l,$$

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where

$$\Phi_i^l = \sum_{n=-\infty}^{\infty} q_i^l[n] q_i^{l*}[n]$$

5 and

$$\mathbf{u}_{i}^{l} = \sum_{n=-\infty}^{\infty} \hat{x}_{i}^{l-1*}[n]\mathbf{q}_{i}^{l}[n].$$

Joint Transmitter and Receiver Design

The present invention is further directed to an embodiment in which the transmitter has available at least partial information about the channel state (for example, via a low-delay feedback path from receiver to transmitter). In this situation, significant performance enhancements can be achieved. For example, if the transmitter has exact knowledge of the channel characteristics, part of the equalization can be performed at the transmitter so that noise enhancement and/or error propagation can be avoided. This idea is analogous to Tomlinson-Harashima precoding; see (a) M. Tomlinson, "New automatic equalizer employing modulo arithmetic," *Electon. Lett.*, vol. 7, pp. 138-139, Mar. 1971. (b) H. Miyakawa and H. Harashima, "A method of code conversion for a digital communication channel with intersymbol interference," *Trans. Inst. Electron. Commun. Eng. Japan*, vol 52-A, pp. 272--273, June 1969. (c) H. Harashima and H. Miyakawa, "Matched-transmission technique for channel with intersymbol interference," *IEEE Trans. Commun.*, vol. COM-20, pp. 774--780, Aug. 1972.

While this invention has been particularly shown and described with references to preferred embodiments thereof, it will be understood by those skilled in the art that various changes in form and details may be made therein without departing from the spirit and scope of the invention as defined by the appended claims.

For example the first and second filters specified above may comprise linear, non-

linear, time-variant, time-invariant, infinite-impulse response, and finite impulse response filters. Although the above description of the preferred embodiment of the present invention pertains to equalization of digital signals, the principles of the present invention apply equally well to equalization of analog signals, using, for example, analog equivalents of the first and second filters, the combiner, and the slicer.

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CLAIMS

We claim:

5	1.	An iterative equalizer for a data communication system for recovering received
		data transmitted over a data channel comprising:

a first filter for filtering received data according to first filter parameters to generate first-filtered data;

a combiner for modifying the first-filtered data with second-filtered data to generate modified data;

a decision device for generating modified tentative decisions based on the modified data, the modified tentative decisions being modified with respect to tentative decisions of a previous iteration; and

a second filter for filtering tentative decisions from a previous iteration according to second filter parameters to generate said second-filtered data;

wherein said first and second filter parameters are based on the received data.

2. The equalizer of claim 1 wherein the first and second filter parameters are modified at each iteration.

3. The equalizer of claim 1 wherein the equalizer is fractionally-spaced in that the received data is sampled at a rate higher than a symbol rate associated with the received data.

4. The equalizer of claim 1 wherein the received data comprises symbol data.

5. The equalizer of claim 1 wherein the first and second filter parameters are modified at each iteration according to channel parameters that are re-estimated at each iteration based on the received data.

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- 6. The equalizer of claim 1 wherein the received data is encoded for error-correction coding, and wherein the decision device comprises an error-correction decoder and further comprising an encoder for error-correction encoding the tentative decisions from a previous iteration.
- 7. The equalizer of claim 1 wherein the first and second filters comprise filter types selected from the group of filter types consisting of: linear, non-linear, time-variant, time-invariant, infinite-impulse-response, and finite-impulse-response filters.
- 8. The equalizer of claim 1 wherein the received data comprises a plurality of received signals received over a plurality of said data channels, and wherein the equalizer further comprises a like plurality of said first filters corresponding to the plurality of channels.
- 9. The equalizer of claim 1 wherein the received data comprises combined data for a plurality of users, and wherein the equalizer further comprises a like plurality of said second filters corresponding to the plurality of users.
- 20 10. The equalizer of claim 1 wherein the first filter, combiner, decision device and second filter are distributed among a data channel transmitter and receiver.
 - 11. An iterative equalizer for a data communication system for recovering received data transmitted over a data channel having channel parameters comprising:
 - a first filter for filtering received data according to first filter parameters to generate first-filtered data;
 - a combiner for modifying the first-filtered data with second-filtered data to generate modified data;

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a decision device for generating modified tentative decisions based on the modified data, the modified tentative decisions being modified with respect to tentative decisions of a previous iteration; and

a second filter for filtering tentative decisions from a previous iteration according to second filter parameters to generate said second-filtered data;

wherein said first and second filter parameters are based on an estimate of said channel parameters, and wherein the received data is encoded for error-correction coding, and wherein the decision device comprises an error-correction decoder and further comprising an encoder for error-correction encoding said tentative decisions from a previous iteration.

12. The equalizer of claim 11 wherein said first and second filter parameters are modified at each iteration.

13. An iterative equalizer for a data communication system for recovering received data transmitted over a plurality of data channels comprising:

a first filter for filtering received data according to first filter parameters to generate first-filtered data;

a combiner for modifying the first-filtered data with second-filtered data to generate modified data;

a decision device for generating modified tentative decisions based on the modified data, the modified tentative decisions being modified with respect to tentative decisions of a previous iteration; and

a second filter for filtering tentative decisions from a previous iteration according to second filter parameters to generate said second-filtered data;

wherein said first and second filter parameters are based on an estimate of said channel parameters, and wherein the received data comprises a plurality of received signals received over the plurality of data channels, and wherein the equalizer further comprises a like plurality of said first filters corresponding to the plurality of channels.

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- 14. The equalizer of claim 13 wherein said first and second filter parameters are modified at each iteration.
- 15. An iterative equalizer for a data communication system for recovering received data transmitted over a data channel comprising:
 - a first filter for filtering received data according to first filter parameters to generate first-filtered data;
 - a combiner for modifying the first-filtered data with second-filtered data to generate modified data;
 - a decision device for generating modified tentative decisions based on the modified data, the modified tentative decisions being modified with respect to tentative decisions of a previous iteration; and
 - a second filter for filtering tentative decisions from a previous iteration according to second filter parameters to generate said second-filtered data;

wherein said first and second filter parameters are based on an estimate of said channel parameters, and wherein the equalizer is fractionally spaced in that the received data is sampled at a rate higher than a symbol rate associated with the received data.

- 16. The equalizer of claim 15 wherein said first and second filter parameters are modified at each iteration.
- 17. A method for recovering received data transmitted over a data channel in a data communication system comprising iteratively:
 - first-filtering received data according to first filter parameters to generate first-filtered data;

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modifying the first-filtered data with second-filtered data to generate modified data;

generating modified tentative decisions based on the modified data, the modified tentative decisions being modified with respect to tentative decisions of a previous iteration; and

second-filtering tentative decisions from a previous iteration according to second filter parameters to generate said second-filtered data;

wherein said first and second filter parameters are based on the received data.

18. The method of claim 17 further comprising modifying the first and second filter parameters at each iteration.

- 19. The method of claim 17 wherein the received data is sampled at a rate higher than a sample rate associated with the received data.
- 20. The method of claim 17 wherein the received data comprises symbol data.
- 21. The method of claim 17 further comprising modifying the first and second filter parameters at each iteration according to channel parameters that are reestimated at each iteration based on the received data.
- 22. The method of claim 17 wherein the received data is encoded for errorcorrection coding, and wherein generating modified tentative decisions based on
 the modified data comprises error-correction decoding the modified data and
 further comprising error-correction encoding the tentative decisions from a
 previous iteration.
- 23. The method of claim 17 wherein the first and second filters comprise filter types selected from the group of filter types consisting of: linear, non-linear, time-

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variant, time-invariant, infinite-impulse-response, and finite-impulse-response filters.

- 24. The method of claim 17 wherein the received data comprises a plurality of received signals received over a plurality of said data channels, and further comprising first-filtering the received data at a plurality of first filters corresponding to the plurality of channels.
- 25. The method of claim 17 wherein the received data comprises combined data for a plurality of users, and further comprising second-filtering the tentative decisions from a previous iteration at a plurality of second filters corresponding to the plurality of users.
- 26. The method of claim 17 wherein the first filter, combiner, decision device and second filter are distributed among a data channel transmitter and receiver.
- 27. A method for recovering received data transmitted over a data channel having channel parameters, in a data communication system, comprising iteratively:

first-filtering received data according to first filter parameters to generate first-filtered data;

modifying the first-filtered data with second-filtered data to generate modified data;

generating modified tentative decisions based on the modified data, the modified tentative decisions being modified with respect to tentative decisions of a previous iteration; and

second-filtering tentative decisions from a previous iteration according to second filter parameters to generate said second-filtered data;

wherein said first and second filter parameters are based on an estimate of said channel parameters, and wherein the received data is encoded for error-correction coding, and wherein generating modified tentative decisions based on

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the modified data comprises error-correction decoding the modified data and further comprising error-correction encoding the tentative decisions from a previous iteration.

- 5 28. The method of claim 27 further comprising modifying the first and second filter parameters at each iteration.
 - 29. A method for recovering received data transmitted over a plurality of data channels having channel parameters, in a data communication system, comprising iteratively:

first-filtering received data according to first filter parameters to generate first-filtered data;

modifying the first-filtered data with second-filtered data to generate modified data:

generating modified tentative decisions based on the modified data, the modified tentative decisions being modified with respect to tentative decisions of a previous iteration; and

second-filtering tentative decisions from a previous iteration according to second filter parameters to generate said second-filtered data;

wherein said first and second filter parameters are based on an estimate of said channel parameters, wherein the received data comprises a plurality of received signals received over the plurality of data channels, and further comprising first-filtering the received data at a plurality of first filters corresponding to the plurality of channels.

- 30. The method of claim 29 further comprising modifying the first and second filter parameters at each iteration.
- 31. A method for recovering received data transmitted over a data channel having channel parameters, in a data communication system, comprising iteratively:

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first-filtering received data according to first filter parameters to generate first-filtered data;

modifying the first-filtered data with second-filtered data to generate modified data;

generating modified tentative decisions based on the modified data, the modified tentative decisions being modified with respect to tentative decisions of a previous iteration; and

second-filtering tentative decisions from a previous iteration according to second filter parameters to generate said second-filtered data;

wherein said first and second filter parameters are based on an estimate of said channel parameters, and wherein the received data is sampled at a rate higher than a symbol rate associated with the received data.

32. The method of claim 31 further comprising modifying the first and second filter parameters at each iteration.

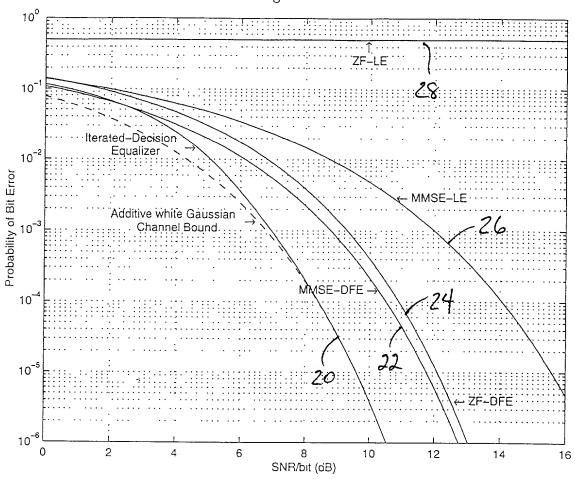
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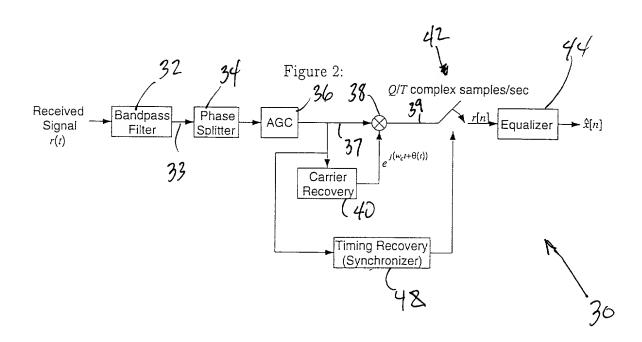
BLOCK-ITERATIVE EQUALIZERS FOR DIGITAL COMMUNICATION SYSTEMS

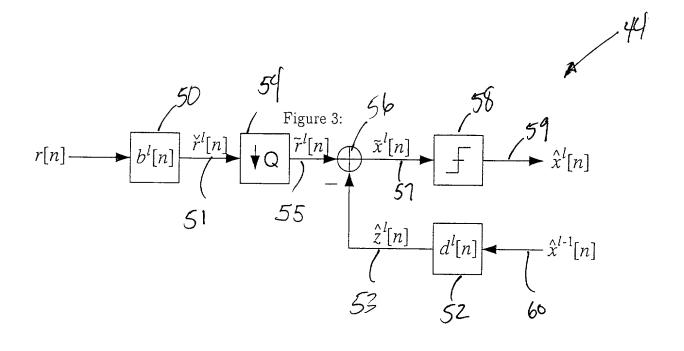
ABSTRACT OF THE DISCLOSURE

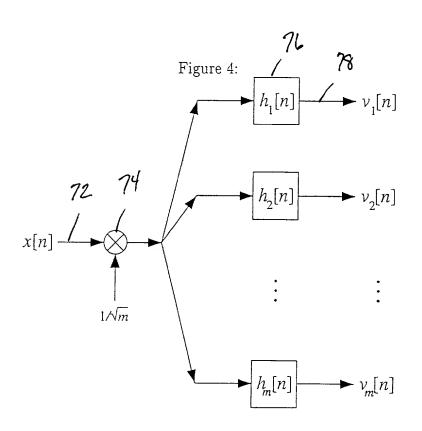
A block-iterative equalizer is adapted for use in contemporary digital communication system receivers. In a preferred embodiment, data received over a communication channel is processed by a linear feed-forward filter and the resulting filtered signal is provided to a slicer which makes a first set of tentative symbol decisions. During later iterations, the same received data is processed by the linear feed-forward filter, the feed-forward filter parameters being modified at each iteration based on the received data and the tentative decisions are themselves filtered by a second, "feed-back" filter and used to improve the tentative decisions.

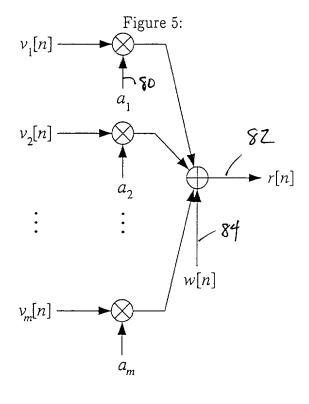
Figure 1:

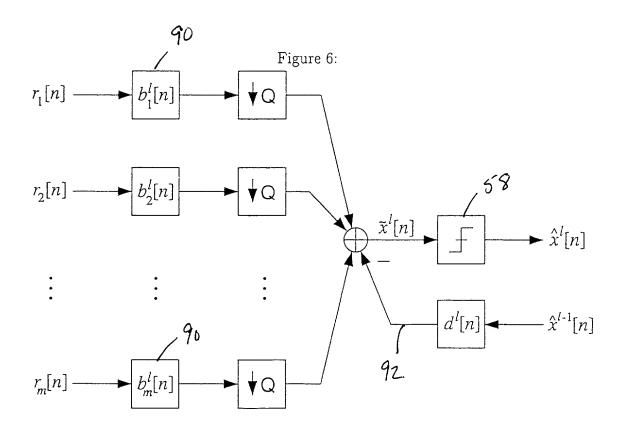


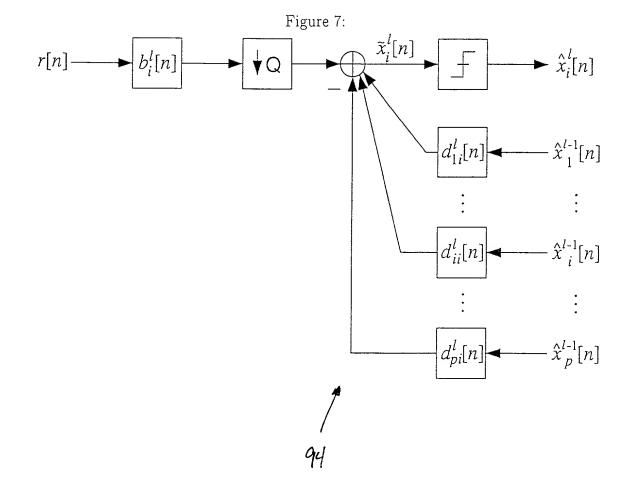












DECLARATION AND POWER OF ATTORNEY

We, the below named inventors, hereby declare that:

Our residences, post office addresses, and citizenships are as stated below next to our respective names.

We believe we are the original, first, and joint inventors of the subject matter which is claimed and for which a patent is sought on the invention entitled **BLOCK-ITERATIVE EQUALIZERS FOR DIGITAL COMMUNICATION SYSTEM**, the specification of which is attached hereto.

We hereby state that we have reviewed and understand the contents of the above identified specification, including the claims.

We acknowledge the duty to disclose information which is material to patentability in accordance with Title 37, Code of Federal Regulations, Section 1.56.

We hereby declare that all statements are made hereby of our own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

And we hereby appoint:

-	Reg. No. 20,798
-	Reg. No. 24,445
-	Reg. No. 33,298
-	Reg. No. 35,192
-	Reg. No. 35,305
-	Reg. No. 35,985
-	Reg. No. 36,610
-	Reg. No. 38,572
-	Reg. No. 44,357
	-

all of the firm of Samuels, Gauthier & Stevens, our attorneys with full power of substitution and revocation, to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith.

We request that all correspondence be directed to:

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